

01/15/2004

SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 43876-155		SERIAL NO. Continuation of Serial No. 10/646,787	
				APPLICANT Craig HANSEN et al			
				FILING DATE Jan. 15, 2004		GROUP To be assigned	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US 5,819,101	10/6/1998	Alexander Peleg, et al			
		US 5,881,275	3/9/1999	Alexander Peleg, et al			
		US 6,119,216	9/12/2000	Alexander Peleg, et al			
		US 6,516,406	2/4/2003	Alexander Peleg, et al			
		US 6,539,487	3/25/2003	Timothy D. Anderson, et al			
		US 6,574,724	6/3/2003	David Hoyle, et al			
		US 6,631,389 B2	10/7/2003	Derrick Chu Lin, et al			
		US					
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		US					
FOREIGN PATENT DOCUMENTS							
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						Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
EXAMINER		DATE CONSIDERED					

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

WDC99 866038-1.043876-0155

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,025,772	05/24/77	Constant			
	4,489,393	12/18/84	Kawahara, et al.			
	4,701,875	10/20/87	Konishi, et al.			
	4,727,505	02/23/88	Konishi, et al.			
	4,876,660	10/24/89	Owens, et al.			
	4,893,267	01/09/90	Alsup, et al.			
	4,956,801	09/11/90	Priem et al.			
	4,969,118	11/06/90	Montoye, et al.			
	4,975,868	12/04/90	Freerksen			
	5,032,865	07/16/91	Schlunt			
	5,157,388	10/20/92	Kohn			
	5,201,056	04/06/93	Daniel, et al.			
	5,268,855	12/07/93	Mason, et al.			
	5,268,995	12/07/93	Diefendorff, et al.			
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987					
	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen, IEEE Micro. April 1992, 40-63					
	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.					
	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, Microprocessor Report July 31, 1995, 15-16					
EXAMINER	Henry Lee			DATE CONSIDERED 8/17/06		

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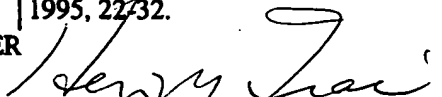
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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,408,581	04/18/95	Suzuki, et al.			
	5,423,051	06/06/95	Fuller, et al.			
	5,426,600	06/20/95	Nakagawa, et al.			
	5,500,811	03/19/96	Corry			
	5,557,724	09/17/96	Sampat, et al.			
	5,588,152	12/24/96	Dapp, et al.			
	5,592,405	01/07/97	Gove, et al.			
	5,640,543	06/17/97	Farrell, et al.			
	5,642,306	06/24/97	Mennemeier, et al.			
	5,666,298	09/09/97	Peleg, et al.			
	5,669,010	09/16/97	Duluk, Jr.			
	5,673,407	09/30/97	Poland, et al.			
	5,675,526	10/07/97	Peleg, et al.			
	5,680,338	10/21/97	Agarwal, et al.			

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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	0 474 246 A2	06/09/91	EP				
	0 654 733 A1	05/07/94	EP				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
	The Visual Instruction Set (VIS) in UltraSPAR™, L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, IEEE, May 3, 1995, 462-469 Osborne McGraw-Hill, i860™ Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn, 1990, 8-10; 171-175; 182-183
	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998, 5-26
	Ruby B. Lee, "Accelerating Multimedia with Enhanced Microprocessors", IEEE Micro, April 1995, 22-32.

EXAMINER 	DATE CONSIDERED 8/17/06
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43876-155

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 APPLICANT
HANSEN, et al.

 FILING DATE
August 25, 2003

GROUP

(PTO-1449)

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	5,408,581	04/18/95	Suzuki, et al.			
	5,423,051	06/06/95	Fuller, et al.			
	5,426,600	06/20/95	Nakagawa, et al.			
	5,500,811	03/19/96	Corry			
	5,557,724	09/17/96	Sampat, et al.			
	5,588,152	12/24/96	Dapp, et al.			
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	5,675,526	10/07/97	Peleg, et al.			
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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
25	0 474 246 A2	06/09/91	Japan				
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	The Visual Instruction Set (VIS) in UltraSPAR TM , L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, May 3, 1995.
	Osborne McGraw-Hill, 1860 th Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn
	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3). Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998.
	Accelerating Multimedia with Enhanced Microprocessors, Ruby B. Lee, 1995.

EXAMINER	DATE CONSIDERED
<i>Henry Tran</i>	8/7/06

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EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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	5,757,432	05/26/98	Dulong, et al.			
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	5,802,336	09/01/98	Peleg, et al.			
	5,809,292	09/15/98	Wilkinson, et al.			
	5,818,739	10/06/98	Peleg, et al.			
	5,825,677	10/20/98	Agarwal, et al.			
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	5,922,066	07/13/99	Cho, et al.			
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EXAMINER	<i>Henry Lee</i>			DATE CONSIDERED		
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		US 5,280,598	01/1994	Osaldi et al.	
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		US 5,487,024	01/1996	Girardeau Jr.	
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		US 6,295,599	09/2001	Hansen et al.	

Repeat IDS
filed 12/23/03

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EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)
		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995)
		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).
		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).
		MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mfg. Part No. M8-00040, (1990).

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